Declassified/Released US Department of State EO Systematic Review 30 JUN 2005

Message Text

CONFIDENTIAL

PAGE 01 STATE 115774

46
ORIGIN EB-11

INFO OCT-01 EA-11 ISO-00 DODE-00 NSAE-00 RSC-01 TRSE-00

AEC-11 CIAE-00 COME-00 MC-02 ACDA-19 AID-20 /076 R

DRAFTED BY EB/ITP/EWT:JRTARTTER:BH
APPROVED BY EB/ITP/EWT:RBWRIGHT
EA/J - MR. PIEZ
---------- 129952
P 031709Z JUN 74
FM SECSTATE WASHDC
TO AMEMBASSY TOKYO PRIORITY

C O N F I D E N T I A L STATE 115774

E.O. 11652: GDS
TAGS: ESTC
SUBJECT: COCOM: JAPANESE REQUEST FOR BILATERALS ON
COMPUTERS

REF: TOKYO 7118
REQUESTED SUMMARY OF PROPOSED US IL 1565 POSITION FOLLOWS.
IN TRANSMITTING IT, YOU SHOULD EMPHASIZE THAT IT WOULD NOT
RPT NOT BE APPROPRIATE TO DISCUSS WITH REPRESENTATIVES
OF JAPANESE COMPUTER INDUSTRY OR TO ALLOW ITS SUBSTANCE TO
BECOME KNOWN OUTSIDE GROUP OF INTERESTED GOJ OFFICIALS.
PROPOSED POSITION HAS NOT BEEN DISCUSSED WITH US INDUSTRY.

1. COMPUTERS UP TO FOLLOWING PARAMETERS COULD BE EXPORTED
TO ALL DESTINATIONS ON AN ADMINISTRATIVE EXCEPTION BASIS
(NATIONAL DISCRETION WITH POST FACTO REPORTING TO COCOM
AND SUBJECT ONLY TO NORMAL CIVIL END USE CONDITIONS OF
PRESENT IL 1565, NOTE 2, PARTS (A) AND (B)).

A. SUM OF I/O AND CPU BUS RATES NOT TO EXCEED 16 MILLION
BITS/SEC.

B. PROCESSING DATA RATE (PDR) OF UP TO 4 MILLION BITS/SEC.

CONFIDENTIAL

CONFIDENTIAL

PAGE 02 STATE 115774

C. INTERNAL MEMORY 1.2 MILLION BITS.

Declassified/Released US Department of State EO Systematic Review 30 JUN 2005

Declassified/Released US Department of State EO Systematic Review 30 JUN 2005

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D. TOTAL EFFECTIVE BIT TRANSFER RATE OF PERIPHERALS 1.2 MILLION BITS/SEC.
E. NO MORE THAN 12 MAGNETIC TAPE TRANSPORTS.

F. TOTAL PERIPHERAL MEMORY CAPACITY 240 MILLION BITS.

G. TOTAL ACCESSES 120 PER SEC.

H. TOTAL EFFECTIVE BIT TRANSFER RATE OF ANY REMOTE TERMINALS UP TO 2400 BITS/SEC AND ANY ONE DEVICE OR COMMUNICATIONS CHANNEL UP TO 1200 BITS/SEC.

2. COMPUTERS WITH A PDR BETWEEN 4 AND 16 MILLION BITS/ SEC WOULD BE SUBJECT TO AUTOMATIC APPROVAL 30 DAYS AFTER SUBMITTED TO COCOM WHEN ACCOMPANIED BY INFORMATION DEMONSTRATING THAT ADDITIONAL END-USE CONDITIONS HAVE BEEN MET. THESE CONDITIONS INCLUDE: EQUIPMENT IS NOT OF A TYPE PRIMARILY USED FOR MILITARY PURPOSES; SPARES ARE UNDER SELLER'S CONTROL; SUPPORT OF SYSTEM TERMINATED IN EVENT OF VIOLATION. EXPORTING GOVERNMENT WILL ALSO BE EXPECTED BEFORE LICENSING TO CONSIDER APPROPRIATENESS OF EQUIPMENT FOR STATED END-USE AND EVIDENCE INDICATING THAT PROPOSED END USER IS INVOLVED IN SIGNIFICANT STRATEGIC ACTIVITIES.

ADDITIONAL PARAMTERS CORRESPONDING TO THE LIMIT OF PDR 16 WOULD INCLUDE THE FOLLOWING:

A. SUM OF I/O AND CPU BUS RATES 80 MILLION BITS/SEC.

B. INTERNAL MEMORY 4.72 MILLION BITS/SEC.

C. EFFECTIVE BIT TRANSFER RATE OF PERIPHERALS 8 MILLION BITS/SEC AND TRANSFER RATE OF ANY PERIPHERAL LIMITED TO 1.5 MILLION BITS/SEC.

D. UP TO 12 MAGNETIC TAPE TRANSPORTS.

E. TOTAL PERIPHERAL MEMORY CAPACITY 1900 MILLION BITS.
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F. TOTAL ACCESSES 250 PER SEC.

G. TOTAL TRANSFER RATE OF REMOTE TERMINALS 9600 AND OF ANY ONE DEVICE 2400 BITS/SEC.

3. COMPUTERS WITH PDR IN RANGE OF 16 TO 32 MILLION BITS/ SEC WOULD BE SUBJECT TO SAME 30-DAY AUTOMATIC APPROVAL

Declassified/Released US Department of State EO Systematic Review 30 JUN 2005

Declassified/Released US Department of State EO Systematic Review 30 JUN 2005

PROCEDURE, CONDITIONS LISTED IN PARA 2 AND FOLLOWING
ADDITIONAL CONDITIONS:

A. MONTHLY VISITATION AND INSPECTION OF FACILITY BY

REPRESENTATIVE OF FIRM, EXCEPT THAT FOR COMPUTERS IN
16-20 PDR RANGE VISITATION WOULD BE REDUCED TO QUARTERLY.

B. SIGNED STATEMENT BY AUTHORIZED OFFICIAL OF IMPORTING
GOVERNMENT OR END USER GOVERNING PEACEFUL END USE, ACCESS
RIGHTS BY REPRESENTATIVE OF FIRM, CHANGE OF CONDITIONS OF
USE, AND DISPOSAL.

C. SIX-MONTH LIMIT ON SPARES.

4. ELECTROMAGNETIC PERIPHERALS WOULD BE REMOVED FROM
EMBARGO.

5. FINAL US PROPOSAL WILL CONTAIN PROVISIONS ON EXPORT
OF TECHNOLOGY AND OEM (I.E. EXPORT OF HARDWARE TO BE
ATTACHED TO COMMUNIST-PRODUCED COMPUTERS) BUT THESE ARE
STILL UNDER STUDY. WE WOULD WELCOME HEARING JAPANESE
VIEWS ON THESE SUBJECTS. KISSINGER

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Declassified/Released US Department of State EO Systematic Review 30 JUN 2005

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Copy: SINGLE
Draft Date: 03 JUN 1974
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Disposition Action: RELEASED
Disposition Approved on Date:
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30 JUN 2005

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